IMPORTANT NOTES ABOUT THIS SCHEMATIC

1) DESIGN NOTES in grey are information notes.

2) DESIGN NOTES in red are critical, and must be understood and followed.

3) A red X indicates suppression of error checking on a pin/net. Commonly suppressed errors include: single-pin net, no driving source, etc.

4) All unique components in this schematic should have a manufacturer’s part number displayed; exceptions to this rule are commodity passives such as resistors and capacitors.

5) Finally, population vs. non-population intent is indicated by adding "NP" next to the part. All parts with "NP" next to the part are intended to be unplaced during assembly.
Skywire socket is two surface mount 2mm pitch 1x150 sockets. MPN: NPPN101BFLC-RC

Skywire socket

DESIGN NOTE:
Sky, On/Off is tied to GND to activate the modem.

DESIGN NOTE:
Sky, On/Off is an active low signal to wake the user device from sleep. It is driven with GPIO0.

Skywire socket

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- Nominal values used, all dimensions in mm
- Pins 2 and 52 (+3.3V), Pins 6 and 48 (+1.5V), each group assigned to same signals on assumption (see PCI_Express_Mini_CEM_12_26Oct07_ncb.pdf, page 50 and 51)
- Room for component keep-out on top layer is approximated about corner arcs.
- Component height rule derived from PCI_Express_Mini_CEM_12_26Oct07_ncb.pdf, Page 18
- Stackup is not specified in PCI_Express_Mini_CEM_12_26Oct07_ncb.pdf nor implemented in this template.